Investigating On-chip Sensor based RPA Attack Vulnerabilities of Lightweight Cipher Algorithms

Final Year Project

Group 18

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In our research...

• Lightweight Ciphers : Ciphers designed to run on Resource Constrained devices

Lightweight Ciphers→ Used in FPGA, IoT, Microcontrollers ...FPGA→ Used in Airbus, Electric Vehicles ...

- Not tested against **Remote Power Analysis** attacks before.
- Most work has been carried out on Xilinx FPGA.
- On our project ⇒ Testing the vulnerabilities of Lightweight Ciphers on Intel Altera FPGAs.





Recap

- Modern cipher algorithms
 - → Highly Mathematically Complex
 - → Nearly impossible to break
- Alternative method : Side Channel Attacks (SCA)
- Side Channel Attack uses:
 - Power Consumption
 - Timing Information
 - Electromagnetic Analysis

to extract secret keys from cryptographic systems



How a Side-Channel Attack is Performed

Recap (continued...)

- Power Analysis : Using power as the side channel.
- CPA^[2]: Correlation Power Analysis is the main method of Power Analysis
- Advisory needs to be present in the premise
- Alternative method
 - → RPA^[3] : **Remote Power Analysis**
- Planting an on chip sensor(hardware design) on victims system.



RPA : Remote Power Analysis

CPA : Correlation Power Analysis

Methodology



Investigating AES Cipher

- 1. Hardware implementation of AES.
- 2. Collected traces for a specific key.
- 3. Target **S-box** operation of AES.
- 4. Consider **one byte** of key at a time.
 - Guess possible keys.



S-box Operation of AES

- Model hypothetical power using **Hamming Distance**^[2] model.
- Hamming Distance (HD): $10010001 \rightarrow 11100001$: 3 (# of bit flips)
- 5. Calculate the correlation coefficient between hypothetical power and actual power consumption.
- 6. Sort key guesses according to correlation coefficient.

SIMON algorithm

- SIMON 32/64
- Has a Feistel structure.
- 64 bit key
 - 4 * 16 bit key blocks
- 32 rounds
 - First 4 round uses 4 key blocks in encryption



$$L_{j}^{i+1} = K_{j}^{i} \oplus R_{j}^{i} \oplus L_{(j+2) \bmod n}^{i} \oplus (L_{(j+1) \bmod n}^{i} \& L_{(j+8) \bmod n}^{i})$$





Investigating SIMON Cipher

- 1. Hardware implementation of Simon(Verilog).
- 2. Collected traces for a specific key.
- 3. Target second round of the SIMON algorithm
- 4. Consider five key bits at a time
 - Guess possible keys.
 - Model hypothetical power using Hamming
 Distance model.
- Calculate the correlation coefficient between hypothetical power and actual power consumption.
- 6. Sort key guesses according to correlation coefficient.





An output bit of 2nd Round Operation

 $HD = HW(L_i^{i+1} \oplus L_i^i)$

Hamming Distance Model

Evaluation of the Attacks

Success Rate^[14] can be used,

 \rightarrow Execute attack n times using same data

 \rightarrow Count successful guesses

Success Rate = $\frac{Number of Successful Attacks}{Total Number of Attacks} \times 100\%$



Experiments and Results

RPA attack results on **AES**

- Performed the RPA attack on AES with 128 bit key.
 - Used Intel Cyclone X FPGA to get traces.
- Used CUDA parallel processing to reduce the runtime
- Evaluated outcomes using the success rate.
- Good baseline for attacking lightweight ciphers



Experimental Setup

RPA attack results on **SIMON**



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Guessed Bits	1,16	1,15	1,9	1,1	2,1
Expected values	0	1	0	1	1

$$\underbrace{(K_1^2 \oplus R_1^2 \oplus L_{15}^2 \oplus (L_{16}^2 \& L_9^2))}_{L_1^3}$$

AND operation of SIMON cipher is vulnerable to RPA attack

Evaluate success rates for RPA on AES

- Success rate vs sample size.
- Sample size > 38,000 \Rightarrow Success rate = 100%.
- AES is 100% vulnerable on Intel FPGA.



Evaluate success rates for RPA on SIMON

- Success rate vs Sample size.
- Sample size > 40,000 \Rightarrow Success rate = 100%.
- SIMON is 100% vulnerable on Intel FPGA.



Comparison of attacks on AES and SIMON

	AES	SIMON			
	128-bit key	64-bit key			
	Number of attacking rounds				
Target	One byte at a time	Five bits at a time			
Number of key guesses in one execution	2 ⁸ = 256	2 ⁵ = 32			
Total number of executions to generate key	256*16 * NOS = 4096 * NOS	32 * 9 * 4 * NOS = 1152 * NOS			

Conclusions

- AES and SIMON are vulnerable to RPA attacks on Intel FPGAs
- **AND** operation of **SIMON** is vulnerable to **RPA** attacks
- When determining remaining keyblocks in SIMON, the error of the previous guesses accumulates

Obtaining Power Traces

RPA Attack on AES

Demonstration

RPA Attack on Simon

Problems and Challenges

- Finding vulnerable points of SIMON to be attacked
 - Two approaches were considered
- Low Power Consumption in SIMON
 - Increase the number of SIMON units
 - After attacking successfully, reduce the number of units
- Having same Correlation values for different guesses
 - Only AND operation is have significant impact on the power traces

• Inaccurate power traces for SIMON

 Changed the values of the TDC delay elements, to identify the vulnerable key bits



Project Outcomes

- The first experiment of **RPA** attacks on **Intel FPGA**s
- The first RPA attack research on Lightweight Ciphers
- Manuscript is in progress
- Peradeniya University Research
 Excellence Showcase 2023

Acknowledgement

For providing Resources

Department of Computer Engineering

For your Guidance and Support

Dr. Darshana Jayasinghe UNSW

Dr. Damayanthi Herath UOP

Dr. Mahanama Wickramasinghe UOP

PURES 2023







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Lightweight Ciphers for Resource Constraint Systems

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Abstract

Cryptography is the art of securing information through mathematical transformations to gain confidentiality, authenticity, and integrity. Modern cipher algorithms, such as Advanced Encryption Standard (AES), are nearly impossible to break with traditional approaches because of their mathematical complexity and large secret key sizes. Cryptanalysis methodology is used to find weaknesses in cryptographic systems to reveal secret information. Side channel attack (SCA) is a cryptanalysis methodology which uses by-products of execution such as power consumption, timing information, and defective computations to extract secret keys from cryptographic systems. Power analysis attacks require physical access to the device to measure power consumption which is typically done using an oscilloscope. Recent research shows that adversaries can embed hardware designs referred to as on-chip sensors to monitor power consumption remotely. The on-chip sensor outputs are changed due to the power consumption of cryptographic circuits. By observing on-chip sensor variations, the adversary can deduce power side channels of cryptographic systems/circuits. Thus far only AES has been demonstrated vulnerable to remote power analysis (RPA) attacks on AMD* Xilinx* Field Programmable Gate Array (FPGA) platforms. This research aims one the observation of the second sec for environments with limited resources (memory, processing power, etc.), such as embedded systems. Even though lightweight ciphers are less computationally complex, they provide almost the same security level as standard cipher algorithms (e.g., AES). We were able to reveal the 128-bit secret key of an AES circuit implemented on an Intel Cyclone 10 FPGA platform and power consumption is measured remotely using a Time-to-Digital Converter (TDC) on-chip sensor. Our present results show that the sample size strongly impacts the RPA attack accuracy. Currently, we are investigating RPA attack vulnerabilities of Simon cipher circuits.

Introduction

- · Cryptography is the process of data encrypting and decrypting for secure data transmission. Cryptographic algorithms depend on the mathematical complexity. Examples are : Industry standard AES algorithm takes about 67 billion years to crack.
- All algorithms have an inevitable weakness : They all run on hardware and side channels of hardware can be used to expose secret information.
- Side Channel Attacks are the type of attacks which are used to attack cryptographic algorithms using physical properties. We use power consumption as the Side Channel and Correlation Power Analysis.



Experimental Setup

- For hardware implementation of the selected lightweight ciphers. (PRESENT, Simon, and Speck) Hardware Descriptive Language (HDL) Verilog is used.
- For the experimental setup (given in the above figure) two key components are used; Altera Cyclone 10 FPGA board, and FT232RL FTDI module
- The FPGA board is being used to demonstrate the data encryption data obtaining processes
- The FTDI module is used to transfer data serially between the FPGA
- board and the computer. Power traces alone with the plaintext and ciphertext pairs and corresponding secret keys are transmitted this way.

Acknowledgement: Department of Computer Engineering. University of Peradeniva, for their invaluable support by providing resources to this research.

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Execution of CPA Attack

Evaluation metrics (Success Rate) : The percentage of successful attacks

against a target system. Execute the attack repeatedly to achieve

Success Rate = (Number of Successful Attacks / Total Number of Attacks) * 100

Results

The sample size is increased by 1000 and when it is 5000, the first sub

When the sample size increases, number of expected sub bytes of the

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last round key also increases in the results. This behaviour is almost

maximum accuracy (accuracy larger than 90% is preferred).

The basic method followed: CPA attack with Hamming Distance model

The targeted subprocess of the cipher algorithm:

 PRESENT cipher: S-box operation Simon cipher: Bitwise AND operation

Speck cipher: Modular Subtraction

byte of the last round key appeared.

similar for all the keys that has been tried.

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